Fibre Channel Controller

Dual Port 4-Gbps Fibre Channel (FC) to PCI Express Controller

ISP2432

- 4-Gbps FC increases aggregate throughput rate to 1.6 GBps in full-duplex mode.
- Native PCI Express™ x4 host bus interface for high throughput applications.
- 200,000 IOPS delivers high I/O transfer rates for storage applications.
- Intelligent interleaved DMA (iIDMA) ensures maximum utilization of all data links.
- Hardware assisted firmware tracing (HAFT) for real-time firmware debug.
- Out-of-order frame reassembly (OoOF) reduces congestion and retransmissions.
- Supports diagnostic monitoring interface (DMI) for detailed transceiver information.
- Management data input output (MDIO) interface to access and modify registers.
- T10 cyclic redundancy check (CRC) ensures end-to-end data integrity across SANs.
- Overlapping protection domains for continuous protection of internal data paths.
- Optional error correcting code (ECC) protection for control structures.
- Cisco VSAN frame tagging allows physical ports to be part of multiple logical networks.
- Multi-ID and N_Port virtualization allows a single port to acquire multiple N_Port IDs.

- 4/2/1 Gbps (auto-negotiation)
- SCSI initiator, target, and initiator/target modes
- Two general purpose input output (GPIO) pins per port
- Available in standard and RoHS compliant/lead free packages
- PCI extended capabilities: Power management, MSI, MSI-X, VPD, AER
- JTAG boundary scan, full scan, and memory BIST

ISP2432 FC Controller. The ISP2432 is the industry’s first, true enterprise class, 4-Gbps to PCI Express x4 controller. The ISP2432 is a highly integrated, single chip design with unprecedented levels of performance, making it an ideal solution for host bus adapters (HBAs), embedded subsystems, and other storage networking devices and applications.

Single Chip Design. The ISP2432 incorporates multiple high-speed RISC processors, FC protocol modules (FPMs), integrated frame buffer memory, a PCI Express x4 interface, and multiple DMA channels into a single application specific integrated circuit (ASIC) package. Each FC port utilizes its own separate internal resource to provide independent FC services and functions; a configuration that provides a robust architecture, allowing each port to operate concurrently with no performance degradation.

High Performance Architecture. The ISP2432 is the highest performing FC controller in the industry, delivering over 200,000 IOPS, nearly 2-GBps throughput, and supports the PCI Express x4 bus interface.

Stable, Proven Firmware. The result of more than 15 years of progressive development and testing, the firmware architecture delivers overall reliability and advanced functionality with its single-chip integration, placing QLogic years ahead of its competitors. In combination with the ASIC hardware, the firmware reduces host intervention and interrupt overhead, executes multiple I/O control blocks from host memory, completes several I/O operations per single interrupt, and handles multithreading to fully utilize host bus and FC bandwidths.

Common Software Interface. Supporting a host software and firmware interface similar to existing and newly released products means rapid adoption of new features, capabilities, and technologies across all major operating systems and hardware platforms. Software development time is drastically reduced with common data structures, system calls, and APIs used across the controller software architecture. A common software interface speeds the development of reliable, interoperable storage products, giving application and system developers greater flexibility to meet time-to-market goals.

Investment Protection. For over 15 years, QLogic has been a technological leader by shipping products that address the current needs of customers, yet providing investment protection to support emerging technologies, standards, and protocols. In addition to supporting legacy bus interfaces, QLogic has a long-term commitment to support next generation bus interfaces, including PCI-X 2.0 and PCI Express. QLogic stands alone in the industry with its product portfolio depth and experience in successfully delivering technological solutions that address the needs of today and tomorrow.
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### TECHNICAL SPECIFICATIONS

**ISP2432**

**Fibre Channel Controller**

- **ISP2432**
- **Fibre Channel Interface**
- **RISC**
- **Host Memory**
- **PCI Express x4 Interface**
- **Frame Buffer**
- **SRAM (for RISC Code/IOCBs)**
- **Flash Memory (for Boot Code)**
- **NVRAM (for Configuration Parameters)**
- **PCI Express x4 (4 Gbps)**
- **800 Mb/s (4 Gbps)**
- **800 Mb/s (4 Gbps)**
- **800 Mb/s (4 Gbps)**
- **256 Gb/s**

### Host Bus Specifications

- **Bus interface**: PCI Express x4 (4 lanes with 2 Gbps maximum aggregate bandwidth)
- **Memory**: Addressable up to 8-MB SRAM per port, 2-MB flash (SPI), and 256-KB NVRAM (SPI)
- **Compliance**: Conforms to PCI Express Base, revision 1.0a, PCI Express Card Electromechanical Specification, revision 1.0a, PCI Bus Power Management Interface Specification, revision 1.1

### Fibre Channel Specifications

- **Data rate**: 4/2/1 Gbps auto-negotiation (4.25/2.125/1.0625 Gbps)
- **Topology**: Point-to-point (N_Port), arbitrated loop (NL_Port), and switched fabric (N_Port)
- **Class of service**: Class 2 and 3
- **Protocols**: FCP (SCSI-FCP), IP (FC-IP), FICON (FC-SB-2), FC-TAPE (FCP-2), FC-VI
- **Compliance**: SCSI-3 Fibre Channel Protocol (SCSI-FCP), Fibre Channel Physical and Signaling Interface (FC-PH), Fibre Channel 2nd Generation (FC-PH-2), Third Generation Fibre Channel Physical and Signaling Interface (FC-PH-3), Fibre Channel—Arbitrated Loop (FC-AL-2), Fibre Channel Fabric Loop Attachment Technical Report (FC-FLA), Fibre Channel Tape (FC-TAPE) profile, SSI Fibre Channel Protocol-2 (FCP-2), Second Generation FC Generic Services (FC-GS-3), Third Generation FC Generic Services (FC-GS-3), Fibre Channel Framing and Signaling (FC-FS)

### Physical Specifications

- **Ports**: Dual 4-Gbps FC
- **Package**: 492-pin thermally enhanced plastic ball grid array (PBGA)
- **Dimensions**: 35 mm × 35 mm
- **Pin pitch**: 1.27 mm

### Environment and Equipment Specifications

- **Temperature**: 100°C junction temperature
- **Airflow**: Airflow is design dependent; however, it must be compliant with the junction temperature of the ISP2432.
- **Voltage**: Core: 1.2V; IO: 3.3V, 2.5V, 1.5V; analog (SERDES): 1.2V
- **Power dissipation**: 4.0 W (maximum)

### Ordering Information

- **ISP2432-A1**: Standard version ships in a single tray (quantity 24) or block of 10 trays (quantity 240)
- **ISP2432-A1R**: RoHS compliant version ships in a single tray (quantity 24) or block of 10 trays (quantity 240)